

Sir:

Transmitted herewith for filing is the Patent Application of:

Inventor: **MING FU LI, JAGAR SINGH, YONG TIAN HOU, NARAYANAN BALASUBRAMANIAN, FUJIANG LIN**

For: **CMOS COMPATIBLE LOW BAND OFFSET DOUBLE BARRIER RESONANT TUNNELING DIODE**

Enclosed are:

- ☒ **6** sheets of drawing(s) - formal.
- ☒ An assignment of the invention to **Agency For Science, Technology And Research and National University of Singapore**
- ☐ An associate power of attorney ☐ Applicant claims small entity status
- ☐ Request & Certification under 35 USC 122(b)(2)(b)(i)

The filing fee has been calculated as shown below:

	(Col. 1)	(Col. 2)	OTHER THAN A SMALL ENTITY	
FOR:	NO. FILED	NO. EXTRA	RATE	FEE
BASIC FEE				\$ 770.
TOTAL CLAIMS	51 -20=	31	x 18 =	\$ 558.
INDEP CLAIMS	6 -3=	3	x 86 =	\$ 258.
			SUB TOTAL	\$ 1,586.
			ASSIGNMENT	\$40.
			TOTAL	\$ 1,626.

☒ Please charge my Deposit Account No. 19-0033 in the amount of \$ 1,626. A duplicate copy of this sheet is enclosed.

☒ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 19-0033. A duplicate copy of this sheet is enclosed.

☒ Any additional filing fees required under 37 CFR §1.16.

☒ Any patent application processing fees under 37 CFR §1.17.

Respectfully submitted,


STEPHEN D. ACKERMAN, REG. NO. 37,761

EXPRESS MAIL CERTIFICATE

Express Mail No. **EV385357920US**

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope addressed to Assistant Commissioner of Patents, Alexandria, VA, 22313-1450. Applicant and/or Attorney requests the date of deposit as the Filing Date.

1/29/04
Date of deposit


Signature / Date

31355 U.S. PTO
10/767275
012904